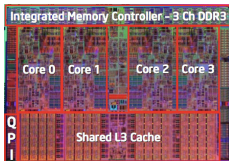


# Satisfying Dataflow Programs Constraints on Multicore Architectures

Citi lab PhD day 2014

Manuel Selva  
Supervisor: Lionel Morel  
Director: Stéphane Frénot  
Bull: Frédéric Soinne

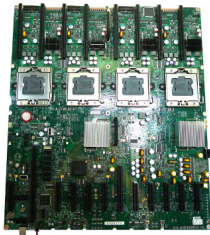
27th March 2014



Nehalem multicore die  
(source: [www.intel.com](http://www.intel.com))



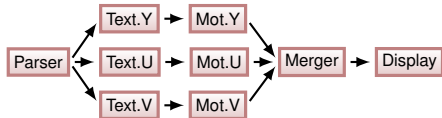
Apple/ARM dual core  
(source: [www.cultofmac.com](http://www.cultofmac.com))



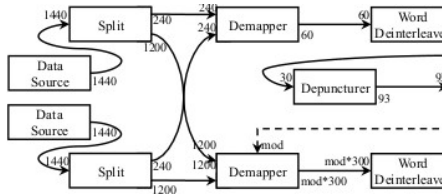
Multiprocessor motherboard  
(source: [www.bit-tech.net](http://www.bit-tech.net))

## How to program ?

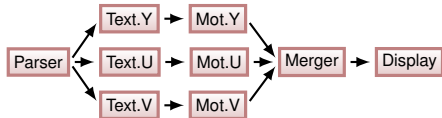
- Threads (Java, C + Pthreads)
- Annotations to sequential code (OpenMP)
- **Dataflow**



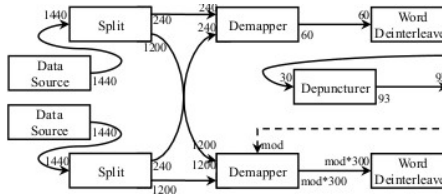
(a) H264 decoding



(b) LTE-Adv decoding

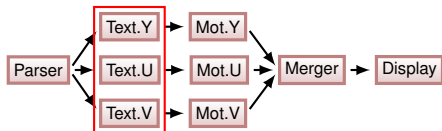


(a) H264 decoding

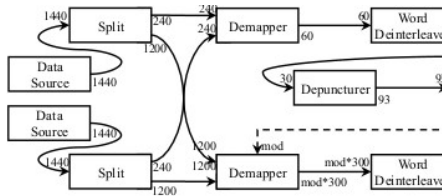


(b) LTE-Adv decoding

- Different kinds of parallelism
  - Actors exchanging data **only** through FIFO channels

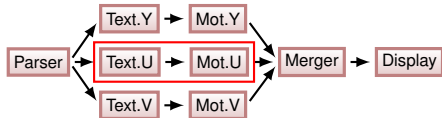


(a) H264 decoding

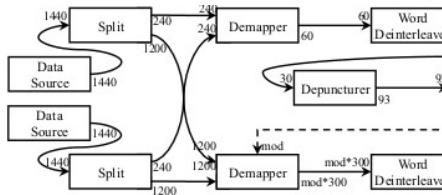


(b) LTE-Adv decoding

- Different kinds of parallelism
  - Actors exchanging data **only** through FIFO channels
  - Task

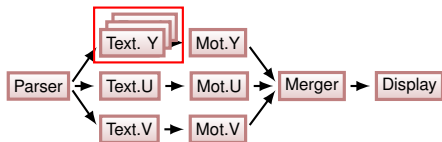


(a) H264 decoding

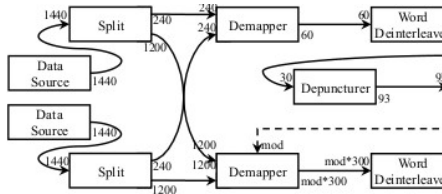


(b) LTE-Adv decoding

- Different kinds of parallelism
  - Actors exchanging data **only** through FIFO channels
  - Task , pipeline

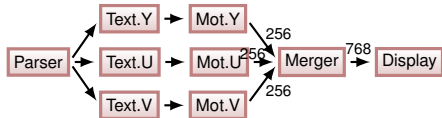


(a) H264 decoding

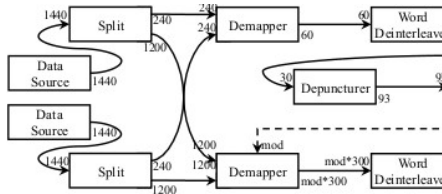


(b) LTE-Adv decoding

- Different kinds of parallelism
  - Actors exchanging data **only** through FIFO channels
  - Task , pipeline , data



(a) H264 decoding



(b) LTE-Adv decoding

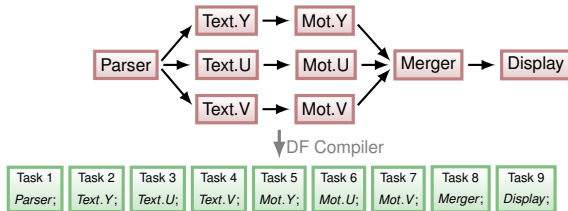
- Different kinds of parallelism
  - Actors exchanging data **only** through FIFO channels
  - Task , pipeline , data

## Many models



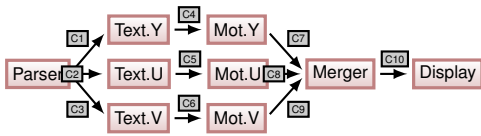


- Compilation to synchronized tasks respecting data dependencies

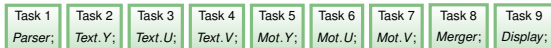


# How to execute dataflow programs ?

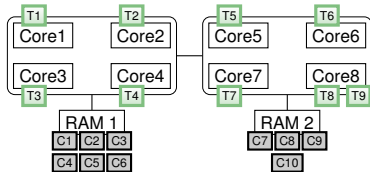
- Compilation to synchronized tasks respecting data dependencies
- Mapping of tasks and channels to hardware



↓ DF Compiler



↓ DF Mapper

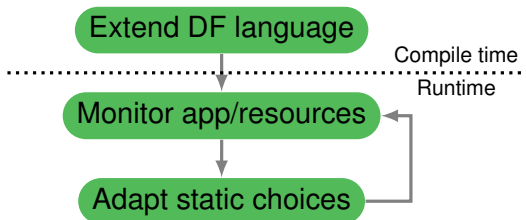


Dual socket processor

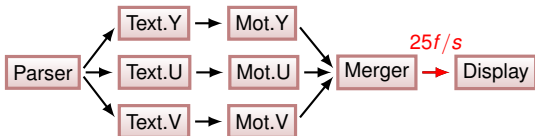
## Motivations

- DF applications with throughput constraints
- Mapping satisfying constraints requires:
  - Actors internal execution time
  - Concurrent applications
  - DF actors consumption/production rates

## Goals

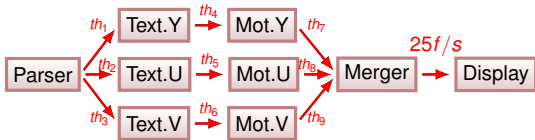


- Languages extensions taken into account in compilation flow [9, 10]



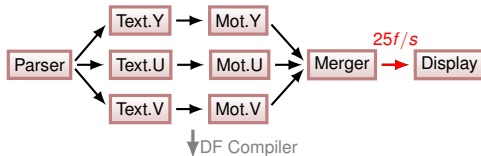
H264 graph with throughput constraint

- Languages extensions taken into account in compilation flow [9, 10]

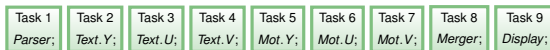


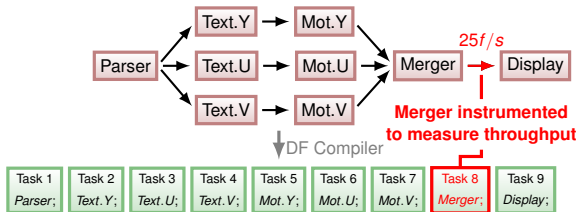
H264 graph with throughput constraint

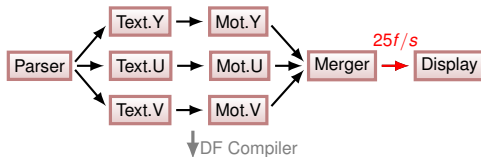
- Propagate this value in SDF languages
  - Determine actors acceptable exec time



↓ DF Compiler





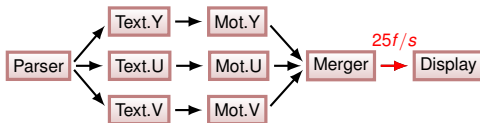


↓ DF Compiler

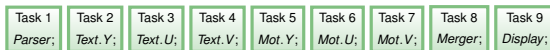


**Tasks instrumented  
to measure  
actors execution times**

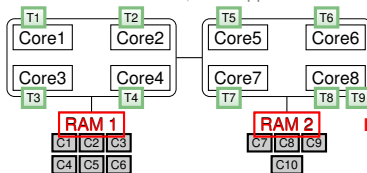




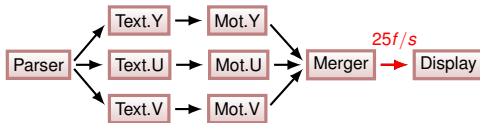
↓ DF Compiler



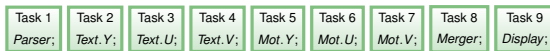
↓ DF Mapper



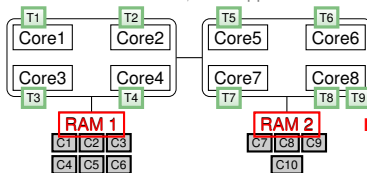
**Memory monitoring using PMU:**  
 RAM controllers load  
 QPI traffic



↓ DF Compiler



↓ DF Mapper



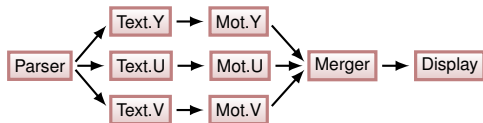
Dual socket processor

**Memory monitoring using PMU:**  
 RAM controllers load  
 QPI traffic

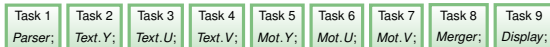
## Conclusions

Are we facing cores load imbalance ?

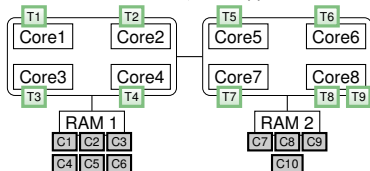
Are the actors too slow because of memory latencies ?



↓ DF Compiler

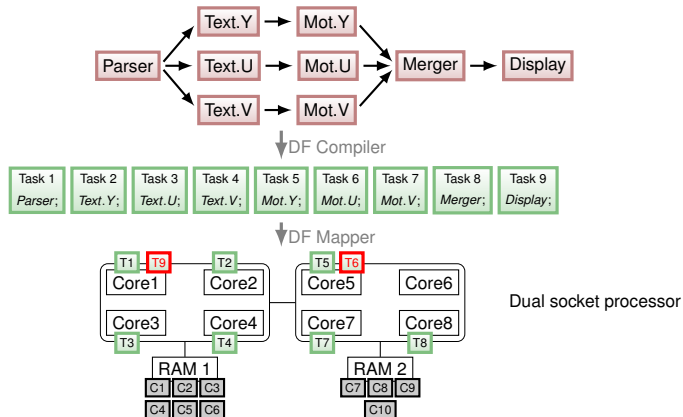


↓ DF Mapper

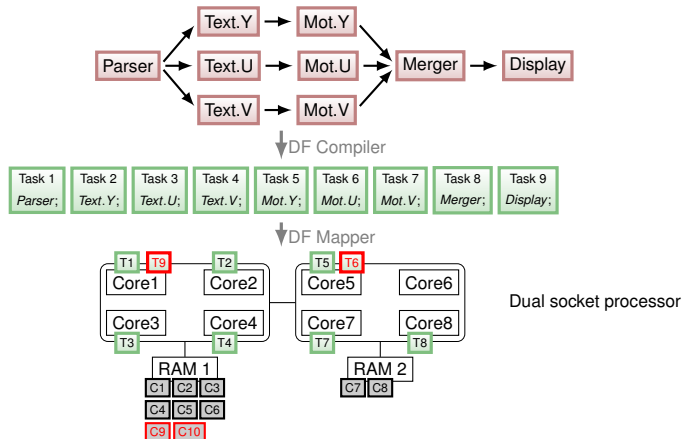


Dual socket processor

- Cpu load balancing



- Cpu load balancing
- Memory load balancing



## Dynamic framework for DF programs

- Applicative monitoring
- Hardware monitoring
- Runtime adaptations making profit of DF information

## Current work

- Finishing implementation in Streamit

## State of the art

- **DF** compilation [7, 13, 4]
- **DF** theoretical throughput analysis [3, 11]
- **DF** adaptation [12, 8, 5, 1]
- **Non-DF** NUMA adaptations [6, 2]



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